


Class	Subclass

**ISSUE CLASSIFICATION**

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PATENT NUMBER

## U.S. UTILITY Patent Application

 O.I.P.E. SCANNED <u>15-3</u> Q.A. <u>Am</u>	PATENT DATE
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APPLICATION NO. 09/965509	CONT/PRIOR D	CLASS 438	SUBCLASS 234	ART UNIT 2812 2818	EXAMINER R. BERRY
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## APPLICANTS

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**TITLE**

## Methods for forming and integrated circuit structures containing enhanced-surface-area conductive layers

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div> </div>			<b>CLAIMS ALLOWED</b>	
				Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div> <div>(Assistant Examiner)</div> <div>(Date)</div> </div>			<b>NOTICE OF ALLOWANCE MAILED</b>	
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